

FIG. 1 is a block diagram of a communication system 100. The system 100 includes a source device 110, a transmission medium 130, and a receiving device 120. The source device 110 is connected to the transmission medium 130, which is connected to the receiving device 120.

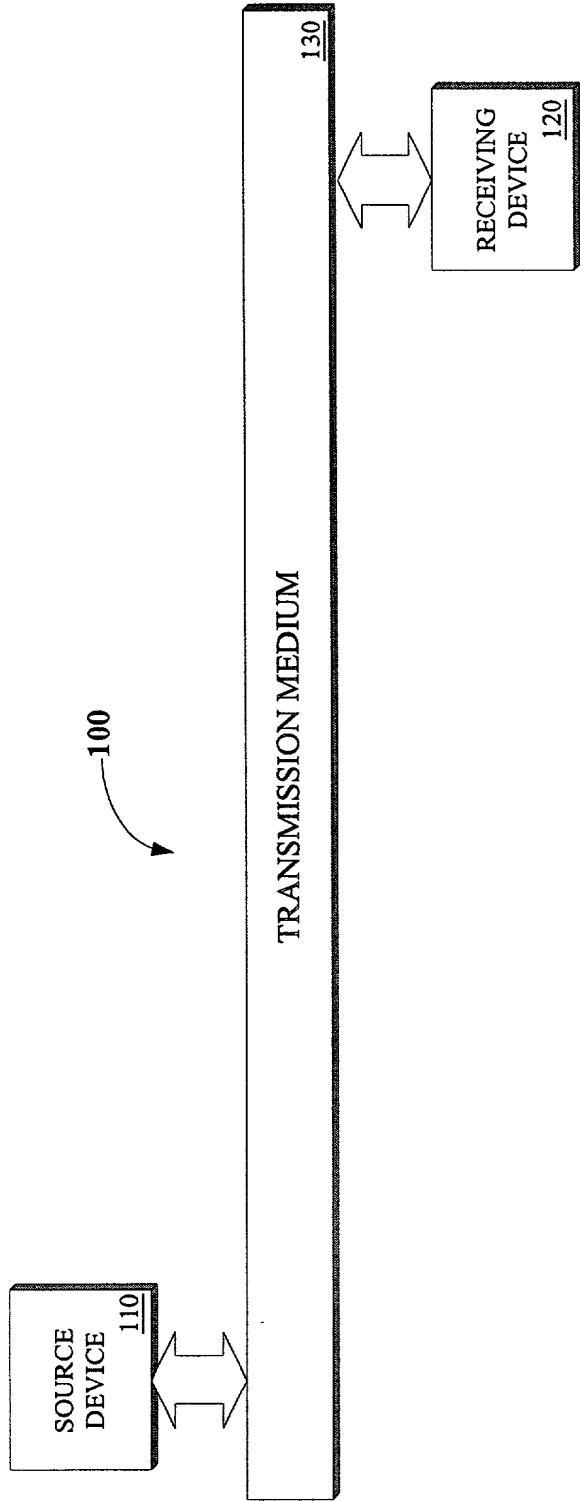


FIG. 1

FIG. 2 is a block diagram of a system 200. The system 200 includes a processor 201, a processor bus 210, a bridge/memory controller 211, memory 213, a bus 220, baseband logic 221, a display device controller 222, a video camera 223, and a network controller 224. The processor 201 is connected to the processor bus 210. The processor bus 210 is connected to the bridge/memory controller 211. The bridge/memory controller 211 is connected to the memory 213. The bridge/memory controller 211 is connected to the bus 220. The bus 220 is connected to the baseband logic 221, the display device controller 222, the video camera 223, and the network controller 224.

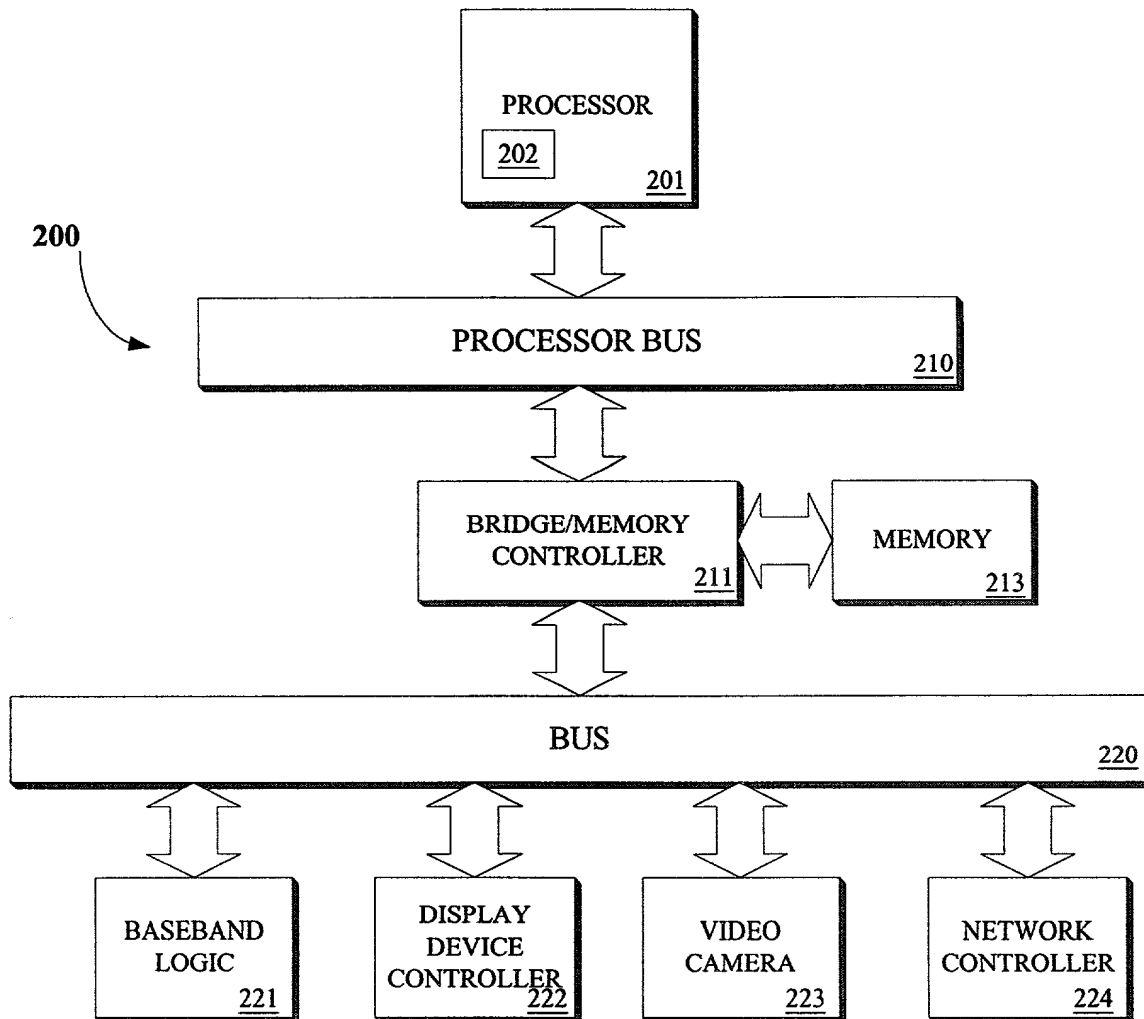


FIG. 2

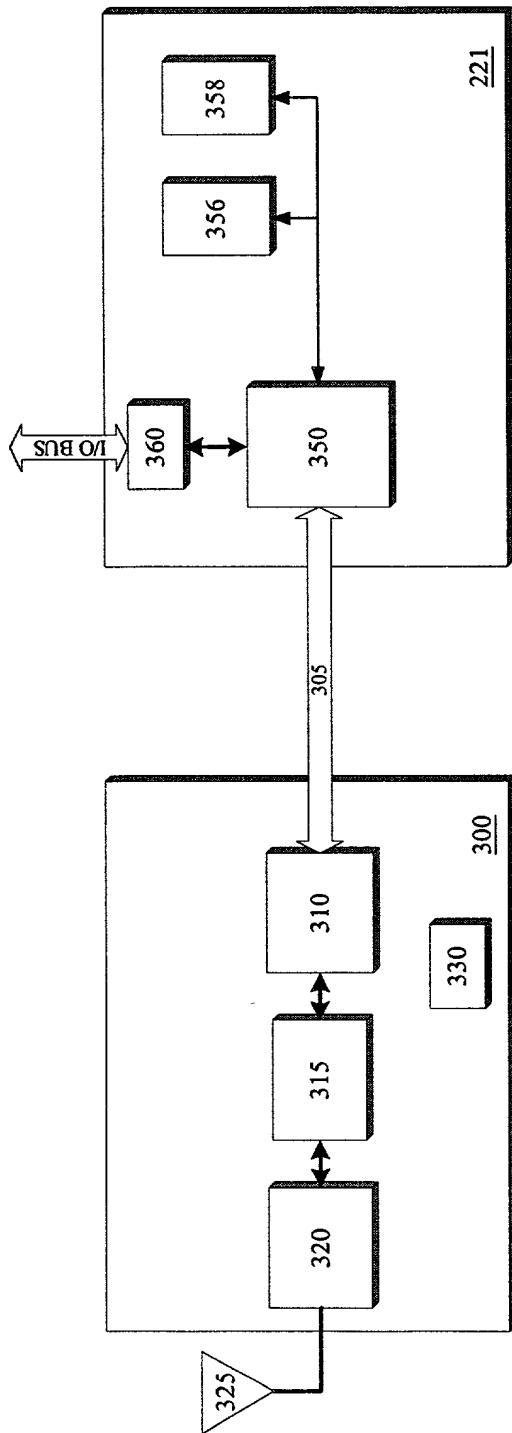


FIG. 3

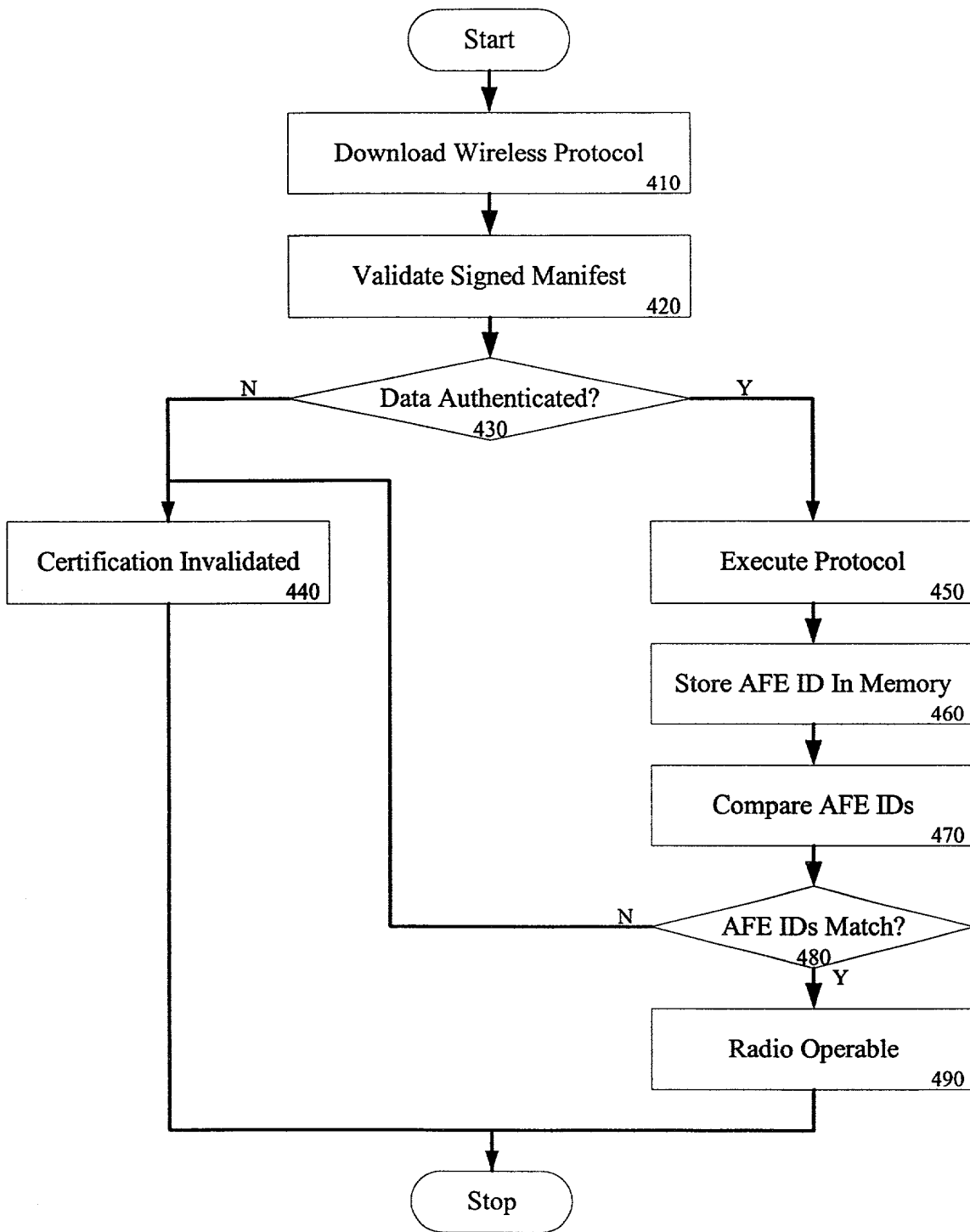


FIG. 4